

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(antifuse adj transistor) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:06
L2	6	(antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:06
L3	1	(mos adj antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:19
L4	1	(mos near antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:19
L5	1	(mos with antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:19
L6	1	(mos same antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:20
L7	0	(mosfet adj antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:20
L8	0	(mosfet near antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:20

EAST Search History

L9	0	(mosfet with antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:21
L10	0	(mosfet same antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:21
L11	0	(metal adj insulator adj semiconductor adj antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:21
L12	1	(metal adj oxide adj semiconductor adj antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:21
L13	0	(metal adj dielectric adj semiconductor adj antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:22
L14	0	((metal adj insulator adj semiconductor) near antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:22
L15	1	((metal adj oxide adj semiconductor) near antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:22
L16	0	((metal adj dielectric adj semiconductor) near antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:22

EAST Search History

L17	0	((metal adj dielectric adj semiconductor) with antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:23
L18	1	((metal adj oxide adj semiconductor) with antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:29
L19	0	((metal adj insulator adj semiconductor) with antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:29
L20	0	((metal adj insulator adj semiconductor) same antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:29
L21	1	((metal adj oxide adj semiconductor) same antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:29
L22	0	((metal adj dielectric adj semiconductor) same antifuse) and (forward adj bias) and (avalanche adj breakdown)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:30
L23	1	(antifuse adj transistor) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:06
L24	18	(antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:06

EAST Search History

L25	1	(mos adj antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:19
L26	1	(mos near antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:19
L27	7	(mos with antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:20
L28	9	(mos same antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:36
L29	0	(mosfet adj antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:20
L30	0	(mosfet adj antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:20
L31	0	(mosfet near antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:20
L32	0	(mosfet with antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:21

EAST Search History

L33	0	(mosfet same antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:21
L34	0	(metal adj insulator adj semiconductor adj antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:21
L35	1	(metal adj oxide adj semiconductor adj antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:21
L36	0	(metal adj dielectric adj semiconductor adj antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:22
L37	0	((metal adj insulator adj semiconductor) near antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:22
L38	1	((metal adj oxide adj semiconductor) near antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:22
L39	0	((metal adj dielectric adj semiconductor) near antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:22
L40	0	((metal adj dielectric adj semiconductor) with antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:23

EAST Search History

L41	1	((metal adj oxide adj semiconductor) with antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:29
L42	0	((metal adj insulator adj semiconductor) with antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:29
L43	0	((metal adj insulator adj semiconductor) same antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:29
L44	1	((metal adj oxide adj semiconductor) same antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:29
L45	0	((metal adj dielectric adj semiconductor) same antifuse) and (forward adj bias) and (avalanche)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:30
L46	87	electrically adj programmable adj antifuses	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:41
L47	132	(ic or (integrated adj circuit)) adj antifuse	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:06
L48	420	257/798.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:06

 PALM IntranetApplication
Number

IDS Flag Clearance for Application 10780427



Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
M844	2005-01-05	21	Y <input checked="" type="checkbox"/>	2005-10-02 14:33:12.0	ISoward
M844	2004-12-17	20	Y <input checked="" type="checkbox"/>	2005-10-02 14:33:12.0	ISoward
M844	2004-02-17	15	Y <input checked="" type="checkbox"/>	2005-10-02 14:33:12.0	ISoward
<input type="button" value="Update"/>					